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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: KIMURA
SERIAL NO.: 10/808,643
FILED: March 25, 2004
FOR: VOLTAGE CONTROL CIRCUIT FOR COMMON MODE ...
GROUP: 2817 Confirmation No. 6102
EXAMINER: Nguyen, Patricia T. DOCKET: NEC 89507

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In connection with the above-entitled matter, Applicant hereby proffers U.S. Patent Office Form PTO-1449, including a copy of the literature reference listed therein. The literature reference was previously submitted, i.e., with Applicant's IDS filed with the initial filing. However, the reference apparently was not considered by the Examiner as "copy not provided". Yet, a copy of that literature reference is found in the file wrapper in private PAIR.

U.S. Patent reference 5,381,113 was submitted with Amendment A, mailed March 13, 2006. Applicants are not submitting a copy of the U.S. Patent reference as the Office waived the requirement on July 11, 2003. These references are discussed in the specification. The claims in the present application are believed to be patentably distinguished over these references.

This information disclosure statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the

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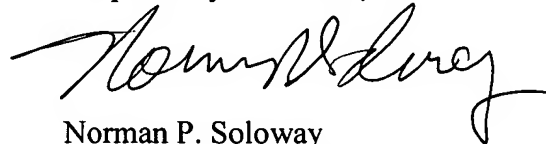
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information thus disclosed in fact constitutes prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.

Since these references were previously brought to the attention of the Examiner,
Applicant believes no fees are due.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,



Norman P. Soloway
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP ISSUE FEE. Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on
June 13, 2006 at Tucson, Arizona.

By: 

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INFORMATION DISCLOSURE CITATION

JUN 16 2006 (Use several sheets if necessary)

Docket Number (Optional)

NEC 89507

Application Number

10/808,643

Applicant(s)

KIMURA

Filing Date

March 25, 2004

Group Art Unit

2817

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,381,113	01.10.1995	Kimura	330	253	

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

		Silva-Martinez, et al., "Design Technique for High-Performance Full-CMOS OTA RC Continuous-Time Filters" IEEE Journal of Solid State Circuits, Vol. 27, No. 7, pp. 993-1001, July 2002.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.